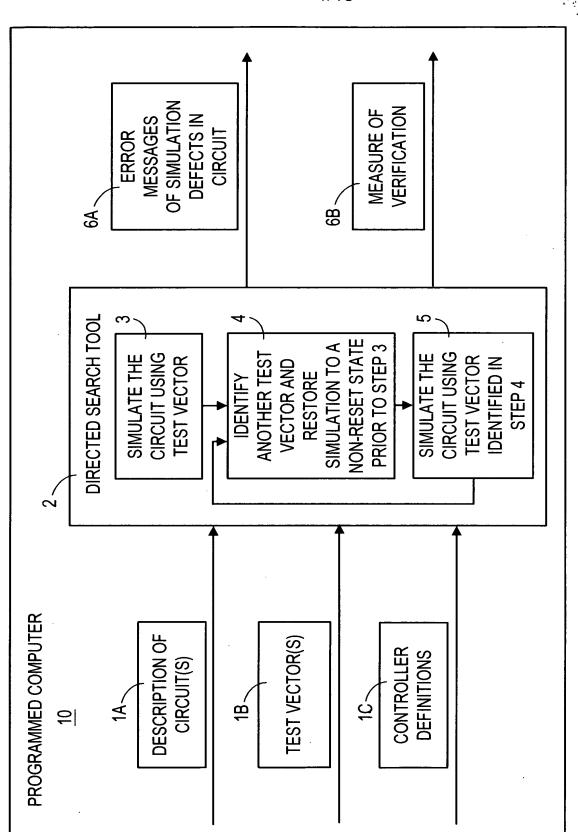
Inventors: Chian-Min R. no, et al.

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Fig. 1A

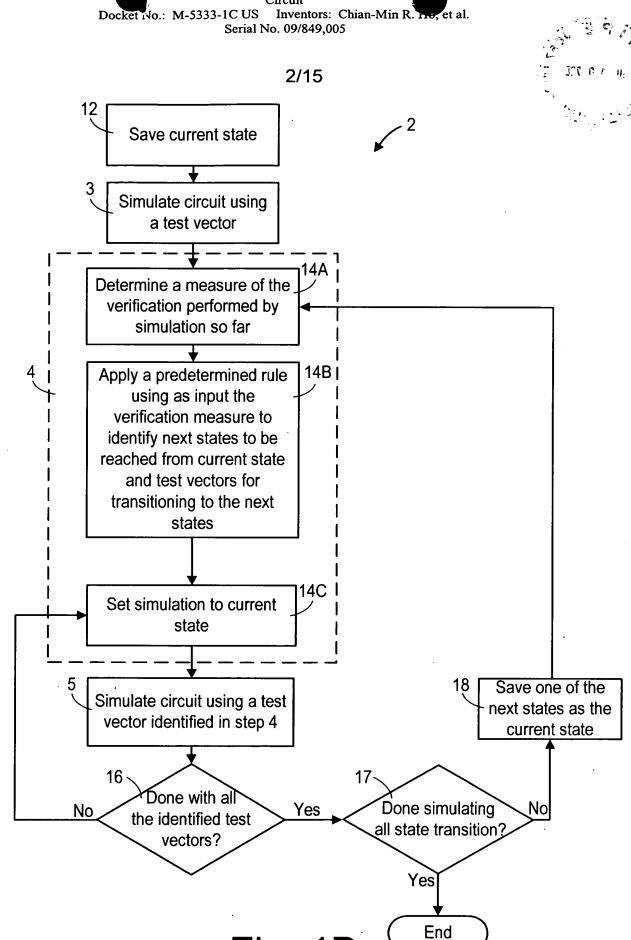


Fig. 1B

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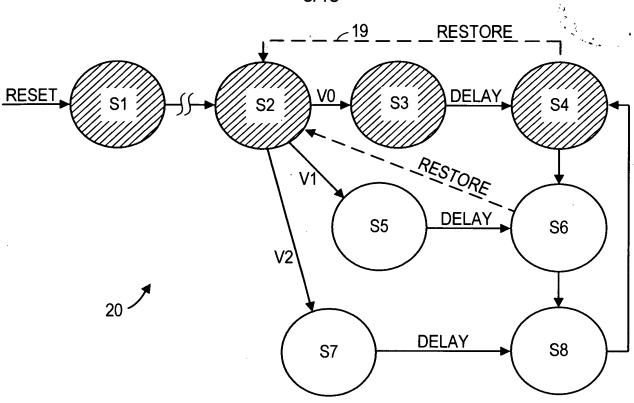


Fig. 1C

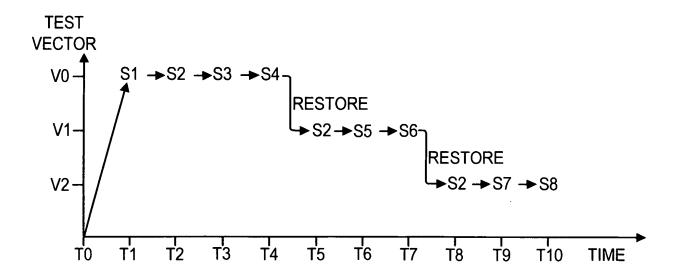


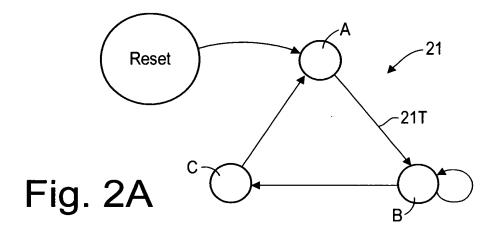
Fig. 1D

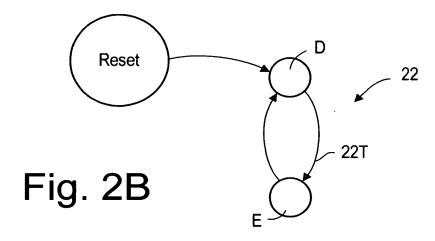
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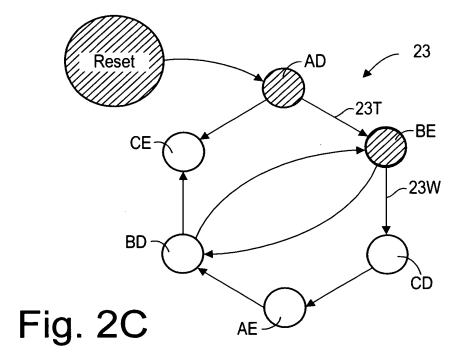
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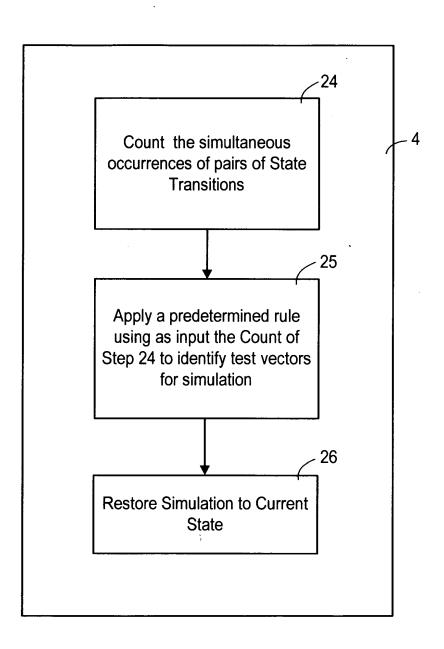


Fig. 2D



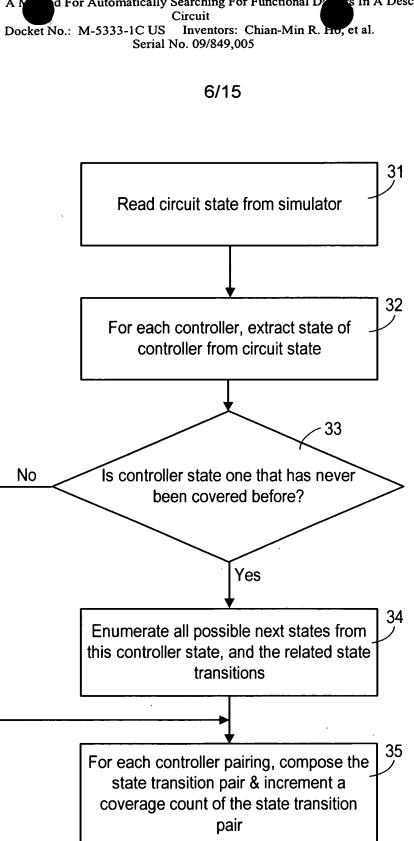


Fig. 2E

USB 49005.UV1901

Circuit

Docket No.: M-5333-1C US Inventors: Chian-Min R. Pro, et al.

Serial No. 09/849,005

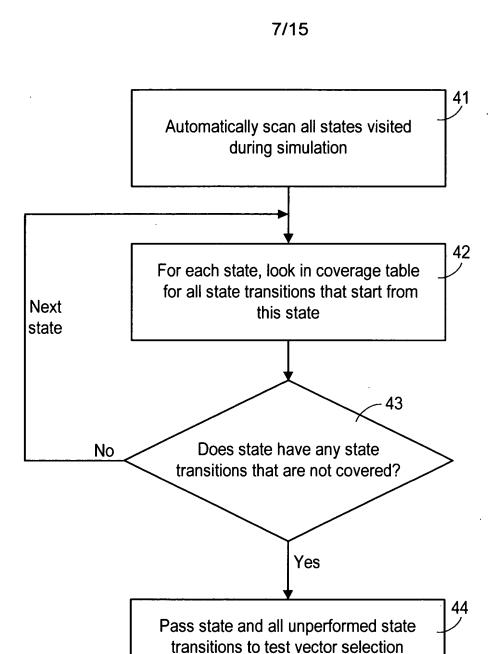


Fig. 2F

function 50

LOBYSOUS "UXISOT

Circuit

Docket No.: M-5333-1C US Inventors: Chian-Min R. Fro, et al.

Serial No. 09/849,005

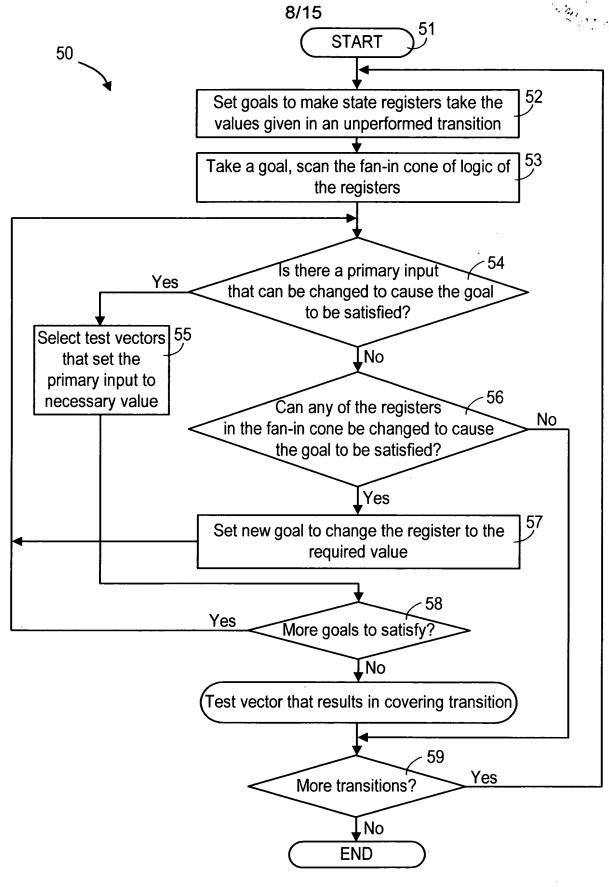


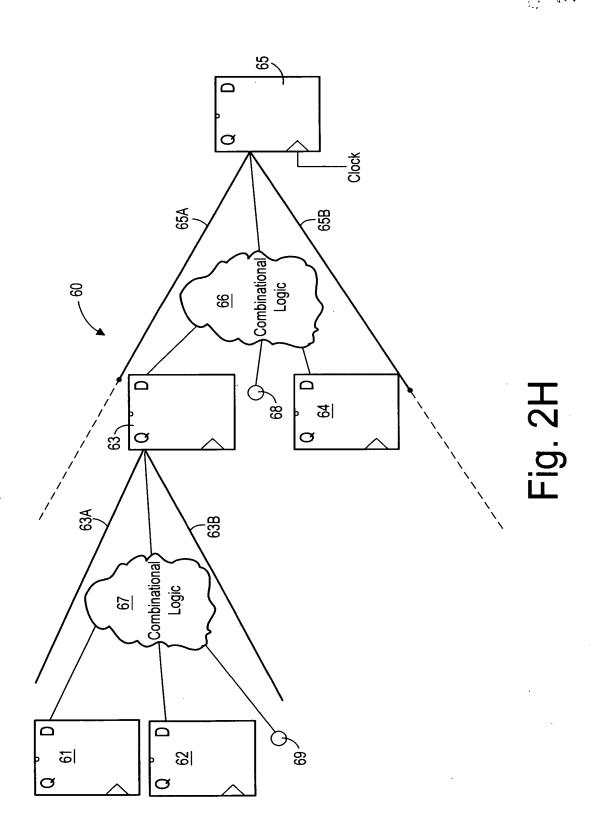
Fig. 2G

Circuit

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Circuit

Docket No.: M-5333-1C US Inventors: C Serial No. 09/849,005 Inventors: Chian-Min R. Ho, et al.

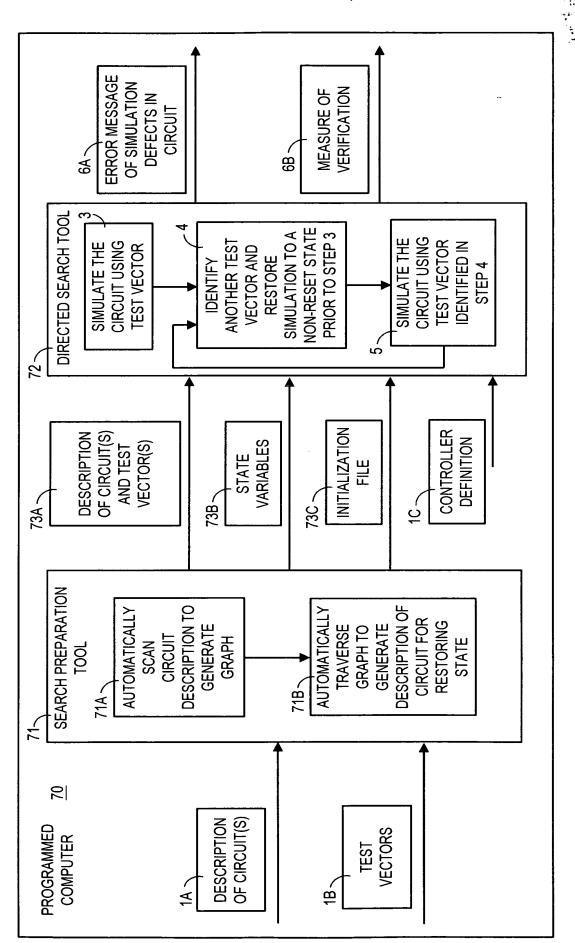
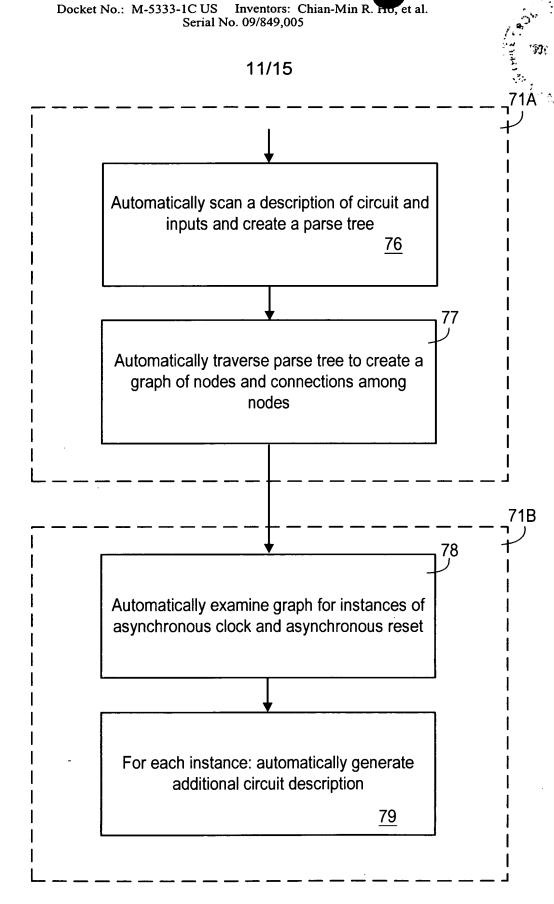
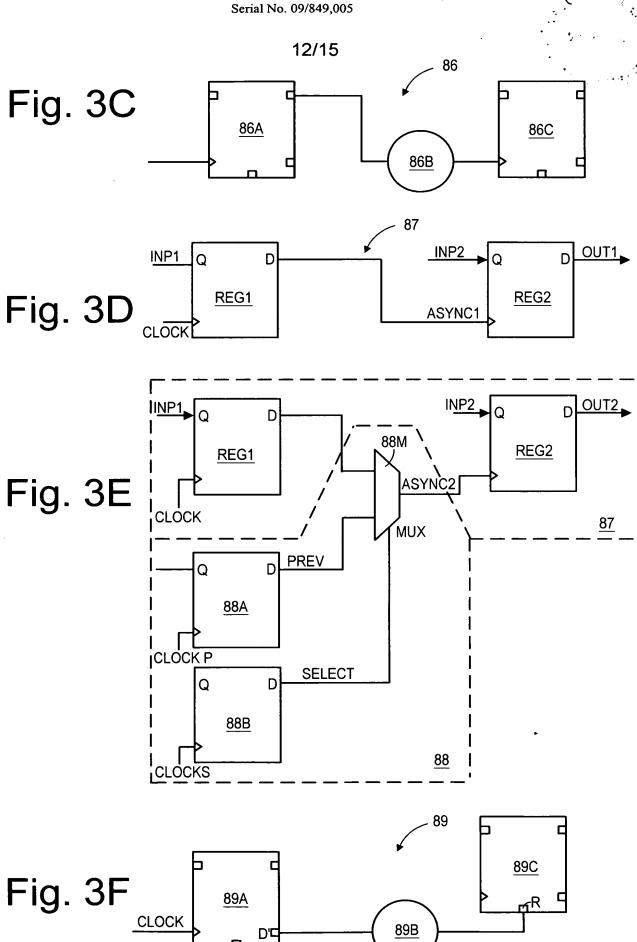


Fig. 3A



DOBUGLOS DVIQUI

Fig. 3B



Circuit

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Write into at least a majority of storage elements of the circuit the values held in Current State

Force each "Previous Value Register" to hold the value of first storage element in state prior to Current State

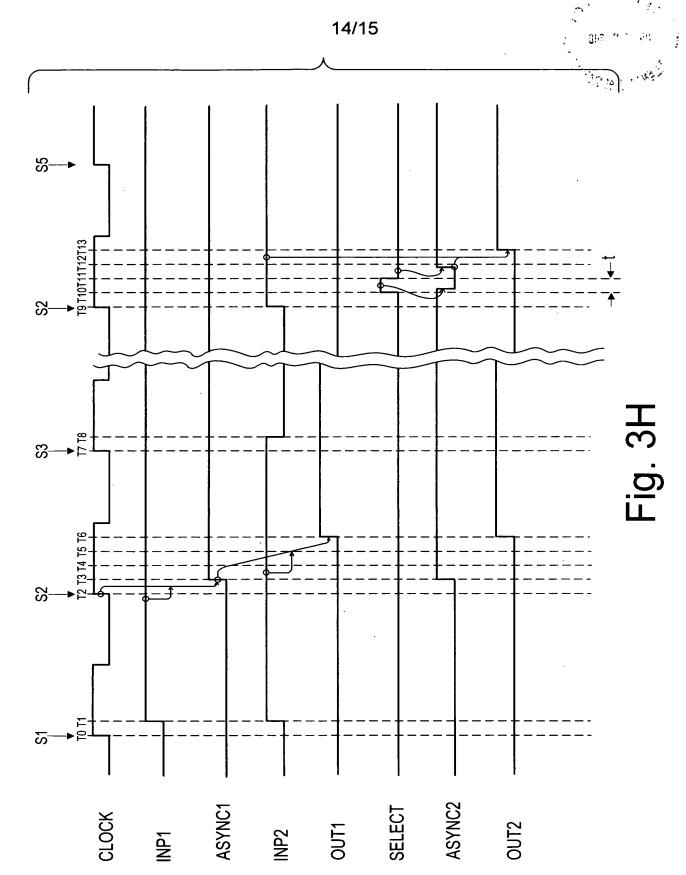
Force each "Asynchronous Mux Select Register" to the value that causes "Mux" to select its input from the "Previous Value Register"

Force the simulation to proceed for a non-zero time period.

(Time period is a small fraction of the simulation cycle period).

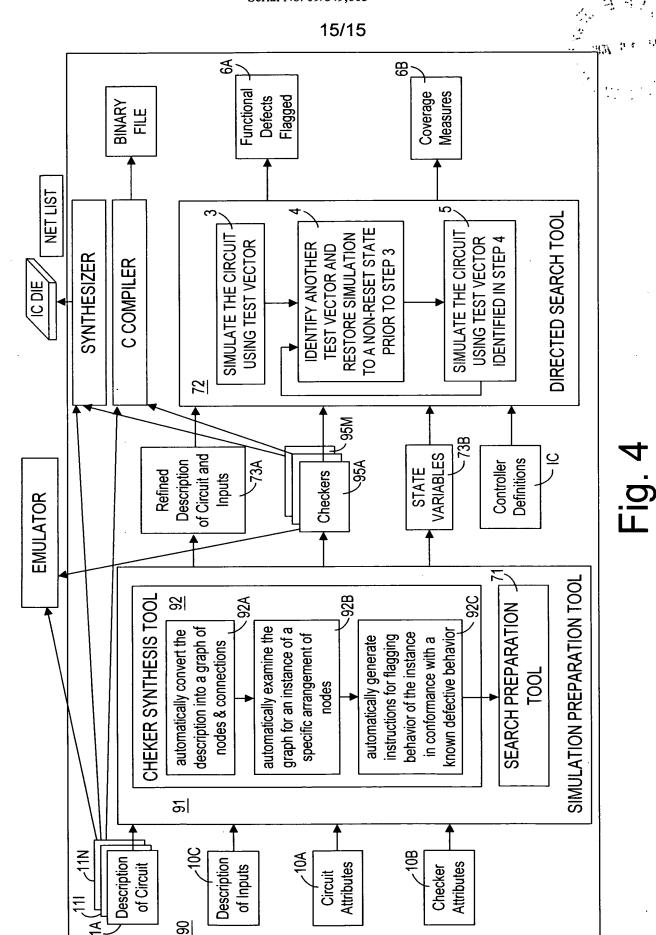
Force each "Asynchronous Mux Select Register" to the value that causes "Mux" to select its input from first storage element

Fig. 3G



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